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PATENT  
Docket No. 10416/2  
APR 30 2001  
TC 2800 MAIL ROOM

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of	)	
Inventor: Terasaki, Yukio	)	Examiner: Unknown
Serial No. 09/741,919	)	
Filed: December 19, 2000	)	Art Unit: 2818
For: CONTROLLER FOR FLASH MEMORY	)	
SYSTEM AND METHOD FOR	)	Date: April 20, 2001
ACCESSING FLASH MEMORY	)	
DEVICE	)	

INFORMATION DISCLOSURE STATEMENT TRANSMITTAL

Assistant Commissioner for Patents  
Washington, D.C. 20231

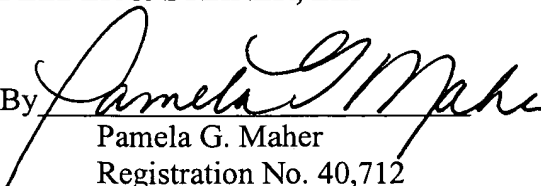
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Sir:

Pursuant to the duty of disclosure and the procedures set forth in 37 CFR §§ 1.56, 1.97 and 1.98, enclosed are copies of the documents listed on the enclosed PTO-FB-A820 form. Because this Information Disclosure Statement has been filed before receipt of a first Office action, no fee has been enclosed. However, in the event this Information Disclosure Statement crosses in the mail with a first Office action, please charge our deposit account no. 02-4270.

Respectfully submitted,

BROWN RAYSMAN MILLSTEIN  
FELDER & STEINER, LLP

By   
Pamela G. Maher  
Registration No. 40,712

BROWN RAYSMAN MILLSTEIN  
FELDER & STEINER, LLP  
1880 Century Park East  
Suite 711  
Los Angeles, California 90067  
Tel: (310) 712-8301  
Fax: (310) 712-8383

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